

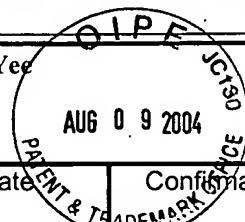
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TRANSMITTAL OF FORMAL DRAWINGS

Docket No.

Yee7

In Re Application Of: Oceager P. Yee

Serial No.
10/748,068Filing Date
12/29/2003

Confirmation No.

Examiner

Art Unit

Invention: SYSTEM AND METHOD FOR DEBUGGING SYSTEM-ON-CHIPS USING SINGLE
OR N-CYCLE STEPPING

Address to:

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Transmitted herewith are:

8 sheets of formal drawing(s) for this application.

Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).

Signature

Thomas J. Onka
Synnestvedt Lechner & Woodbridge LLP
P.O. Box 592
Princeton, NJ 08542
609-924-3773 phone
609-924-1811 fax

Dated:

8/15/04

I certify that this document and attached formal drawings are being deposited on 08/15/04 with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Signature of Person Mailing Correspondence

Thomas J. Onka

Typed or Printed Name of Person Mailing Correspondence